

2816



PATENT APPLICATION  
Do. No. 1482-138

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Barrie Gilbert

Serial No. 09/694,731 Examiner: Quan Tra  
Filed: October 23, 2000 Group Art Unit: 2816  
For: LOW SUPPLY CURRENT RMS-TO-DC CONVERTER  
Date: October 18, 2001

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TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents  
Washington, DC 20231

Responsive to the Office Action dated July 18, 2001, enclosed is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	13	14-20	0	x \$18 =	\$ 0
Independent Claims	2	2-3	0	x \$84 =	\$
Extension of Time - 1 <sup>st</sup>					\$110
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$110

- ☒ Applicant petitions the Commissioner to extend the time for response. The extension fee is included and a duplicate copy of this form is enclosed.
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.
- ☒ Any deficiency or overpayment should be charged or credited to deposit account no. 13-1703.

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.



20575

PATENT TRADEMARK OFFICE

*Joseph S. Makuch*  
Joseph S. Makuch  
Reg. No. 39,286

MARGER JOHNSON & McCOLLOM, P.C.  
1030 SW Morrison Street  
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*11-13-01*  
*[Signature]*



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ARLINGTON, VA 22202-3513

*[Signature]*  
11-19-01

### RESPONSE TO OFFICE ACTION

Responsive to the Office Action, dated July 18, 2001, please amend the application as follows.

### IN THE CLAIMS

Please amend the claims by rewriting as follows.

1. (Amended) A method for operating a transistor cell comprising an input terminal for receiving an input signal, an output terminal for transmitting an output signal, a grounded base transistor coupled between the input and output terminals, and a current mirror coupled between the input and output terminals, the method comprising:

    biasing the transistor cell to establish a bias current in the grounded base transistor and the current mirror when the input signal is zero; and

    limiting the input signal to a range in which the output function of the transistor cell approximates a square-law.

2. (Cancelled)